

REMARKS

Claims 1-8 are pending in the present application. With this Amendment, claims 1, 3, 5, and 7 are amended. Reconsideration of the claim is respectfully requested.

Claim Rejections – 35 USC § 102

Claims 1-8 were rejected under 35 U.S.C. § 102(e) as being anticipated over U.S. Patent No. 6,697,989 to Maeda et al. This rejection is respectfully traversed, especially in view of the amendments above.

In particular, claim 1 has been amended to recite a data processing apparatus including, among other things, wherein:

[T]he syndrome calculation unit enables a syndrome calculation of the demodulated data sequence by detecting a data loss in the first sync frame and the second sync frame on the basis of the address information, performing switching on the basis of the detection of the first demodulated data sequence corresponding to the first sync frame, calculating a first syndrome of the first demodulated data sequence by a first calculation, performing switching on the basis of the detection of the second demodulated data sequence corresponding to the second sync frame, calculating a second syndrome of the second demodulated data sequence by a second calculation, setting the first syndrome as binary 0 on the basis of a loss detection of the first demodulated data sequence, setting the second syndrome as binary 0 on the basis of a loss detection of the second demodulated data sequence, and outputting the syndrome of the demodulated data sequence on the basis of the first syndrome and the second syndrome

Additionally, claim 5 has been amended to recite a data processing method including, among other things:

[E]xecuting a syndrome calculation of the demodulated data sequence containing a first demodulated data sequence and a second demodulated data sequence by detecting a data loss in the first sync frame and the second sync frame on the basis of the address information, performing switching on the basis of the detection of the first demodulated data sequence corresponding to the first sync frame, calculating a first syndrome of the first demodulated data sequence by a first calculation, performing switching on the basis of the detection of the second demodulated data sequence corresponding to the second sync frame, calculating a second syndrome of the second demodulated data sequence by a second calculation, setting the first syndrome as binary 0 on the basis of a loss detection of the first demodulated data sequence, setting the second syndrome as binary 0 on the basis of a loss detection of the second demodulated data sequence, and outputting

the syndrome of the demodulated data sequence on the basis of the first syndrome and the second syndrome

Thus, as amended, the independent claims now recite executing a syndrome calculation containing demodulated data sequences by detecting a data loss in sync frames on the basis of the address information, performing switching on the basis of the detection of the demodulated data sequence corresponding to the sync frame, calculating a syndrome of the demodulated data sequence by a calculation, setting the syndrome as binary 0 on the basis of a loss detection of the demodulated data sequence, and outputting the syndrome of the demodulated data sequence on the basis of the first and second syndromes.

Such an apparatus or method is not disclosed or suggested in Maeda et al. Accordingly, the withdrawal of the rejection and allowance of the claims is respectfully requested.

Claims 2-4 and 6-8 depend from and further limit one of claims 1 and 5 and are allowable at least for the reasons set forth above with respect to claims 1 and 5. Additionally, each claim is further allowable for further defining novel and unobvious features of the present invention. For example, claim 3 has been amended to recite the apparatus of claim 2, further comprising, among other things:

[W]herein the syndrome calculation unit recalculates the syndrome of the demodulated data sequence by detecting a change in order in which the first and second sync frames arrive on the basis of the read-out state of data managed by the management unit, and reading out the syndrome stored in the calculation result storage unit.

Also, claim 7 has been amended to recite the method of claim 6, further comprising:

[S]toring the syndrome calculation result, managing a read-out state of data from the information storage medium for respective first and second sync frames, reading out the stored syndrome calculation result by detecting a change in order in which the first and second sync frames arrive on the basis of the managed read-out state of data by the management unit, and recalculating the syndrome of the demodulated data sequence

Again, Maeda fails to disclose or suggest such an apparatus or method as claimed.

In view of the above amendments and remarks, Applicants respectfully submit that all the claims are allowable and that the entire application is condition for allowance.

Should the Examiner believe that anything further is desirable to place the application in better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number listed below.

Please charge any fees associated with the submission of this paper to Deposit Account Number 033975. The Commissioner for Patents is also authorized to credit any over payments to the above-referenced Deposit Account.

Respectfully submitted,

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